

Patent



Attorney's Docket No.: P17197

UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Patent Application of:

Jiun Hann Sir et al.

Examiner: Phan, Thiem D.

Application No.: 10/691,135

Art Unit: 3729

Filed: October 21, 2003

Title: ROBUST INTERLOCKING VIA

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22131-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b)(2). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with a copy of the patents cited on that form. It is respectfully requested that the cited patents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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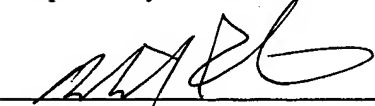
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Respectfully submitted,

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Michael D. Plimier
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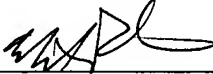
Intel Corporation
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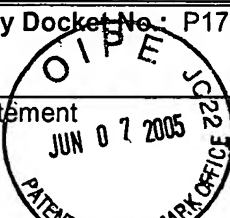
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Form PTO-1449 (Modified)	Atty Docket No.: P17197	Serial No.: 10/691,135
List of Patents and Publications Statement (Use several sheets if necessary)		Applicant: Jiun Hann Sir et al. Filing Date: October 21, 2003

REFERENCE DESIGNATION			U.S. PATENT DOCUMENTS			
Examiner Initials		Document No.		Class	Sub-Class	Filing date if appropriate
	AA	6,441,494 B2	Huang et al.	257	774	
	AB	6,335,565 B1	Miyamoto et al.	257	686	
	AC					
	AD					
	AE					
	AF					
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	AI					
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	AK					
	AL					
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FOREIGN PATENT DOCUMENTS							
No.		Document No.	Date	Country	Class	Sub-Class	Translation
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
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